

Amendments

Please amend Claim 1 as follows:

1. (Amended) A method of constructing a structural model of a memory for use in an ATPG (Automatic Test Pattern Generation), said method comprising the steps of:

accessing a simulation model of said memory, from a simulation library stored in a computer system memory, wherein said simulation model is described in a behavioral hardware description language;

generating a simplified behavioral model of said memory by re-describing said memory with a predefined proper subset of said behavioral hardware description language;

translating, automatically and under control of a computer system, said simplified behavioral model of said memory into said structural model of said memory, wherein said structural model comprises a plurality of ATPG memory primitives; and

storing said structural model in said computer system memory.

3. (Amended) A method as recited in Claim 2 further wherein said simplified behavioral model excludes physical layout information contained in said simulation model of said memory.

Attached hereto is a marked-up version of the changes made to the claims. The previously presented amended Claim 1 contained a couple of inconsistencies. Therefore, the Applicants have re-presented the amendments